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November 16, 2005

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RE: *U.S. Patent Application Serial No. 10/649,051*
Entitled: "INTEGRATED SEMICONDUCTOR STRUCTURE FOR RELIABILITY TESTS OF DIELECTRICS"
Inventor(s): Karsten Wiczorek, Rolf Geilenkeuser and Jorg-Oliver Weidner
Client Reference: DE0349

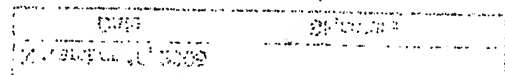
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1. A Fee(s) Transmittal form; and
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The Director is authorized to deduct the issue/publication fees, and any other fees required under 37 C.F.R. §§ 1.16 to 1.21, from the Advanced Micro Devices, Inc. Deposit Account No. 01-0365/DE0349.

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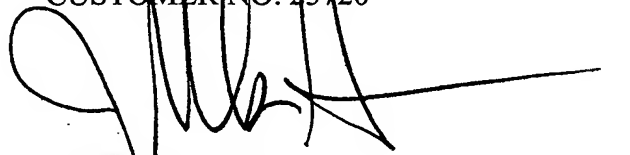
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Respectfully submitted,

WILLIAMS, MORGAN & AMERSON, P.C.
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A handwritten signature in black ink, appearing to read 'J. Mike Amerson', written over a horizontal line.

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Enclosures